



PIC ensures:
 - known state at power on
 - level shift
 - state indication (via LED)

PIC pgm:
 - init Hors Gel
 - receive order on GP2/GP3 via bitbang
 (e.g. clk on GP2, data on GP3)

Pulldowns ensure known init state since rPi GPIOs are inputs at boot
 They can be removed if the target GPIO has an initial Low state, see

<https://www.raspberrypi.org/forums/viewtopic.php?f=44&t=35321>

<http://www.mosaic-industries.com/embedded-systems/microcontroller-projects/raspberry-pi/gpio-pin-electrical-specifications>

OK1 ON / OK2 OFF => LD1G ON => Hors Gel
 OK1 OFF / OK2 ON => LD1R ON => Delestage
 OK1 ON / OK2 ON => LD1R+G ON => Eco
 OK1 OFF / OK2 OFF => LD1 OFF => Confort

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Document Number:	REV:
Date: 7/7/2016 2:10:59 AM	Sheet: 1/1